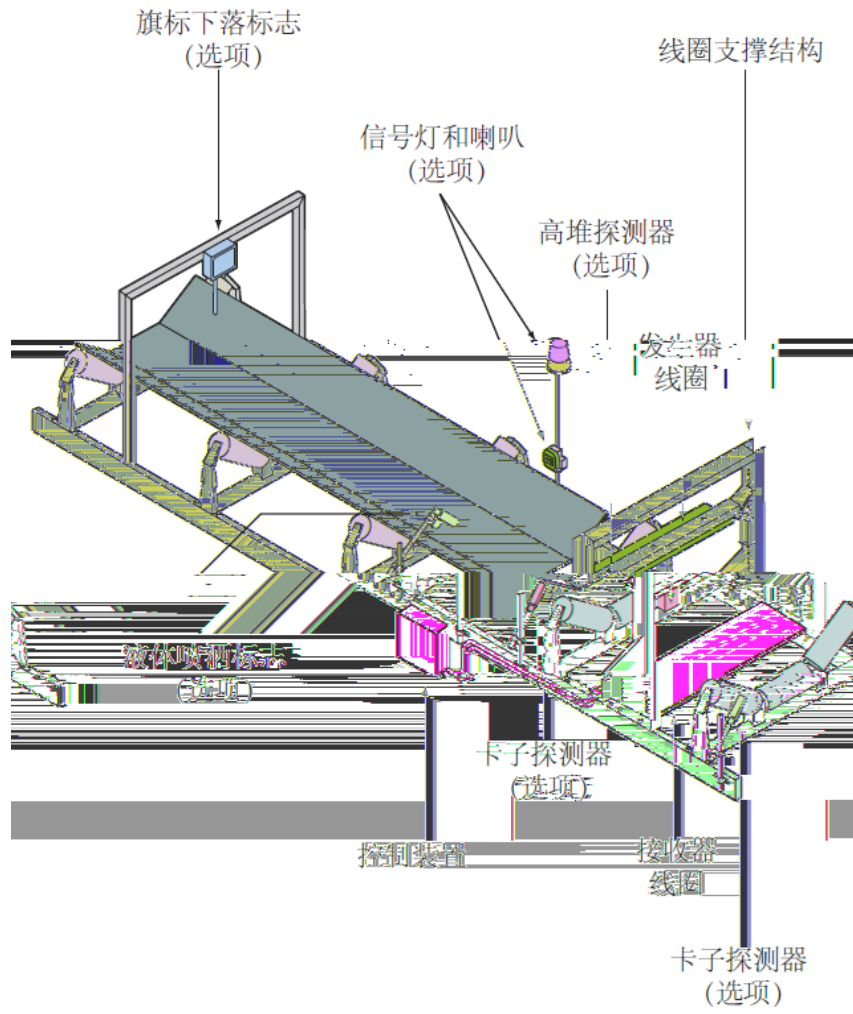


19 HQ-3



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HQ-3

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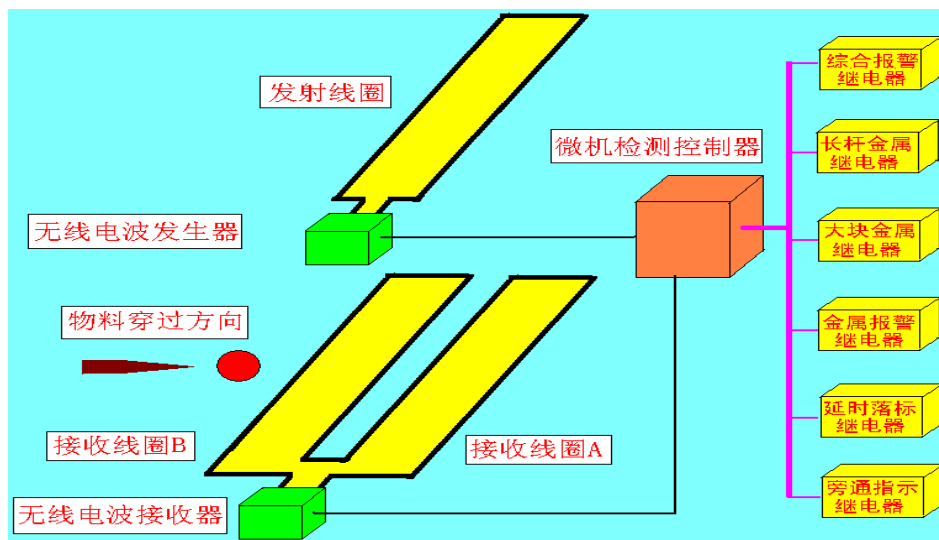
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RS-485

• ADF

DSV

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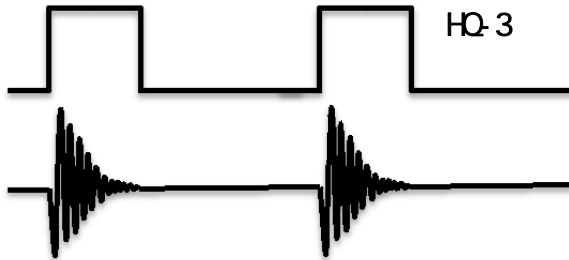
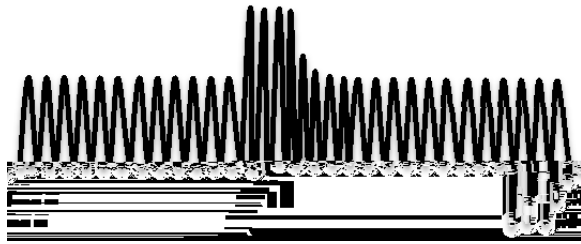
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- ADF() DSV
- PNR QNR VMT
- 20%
- DSP FPGA
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